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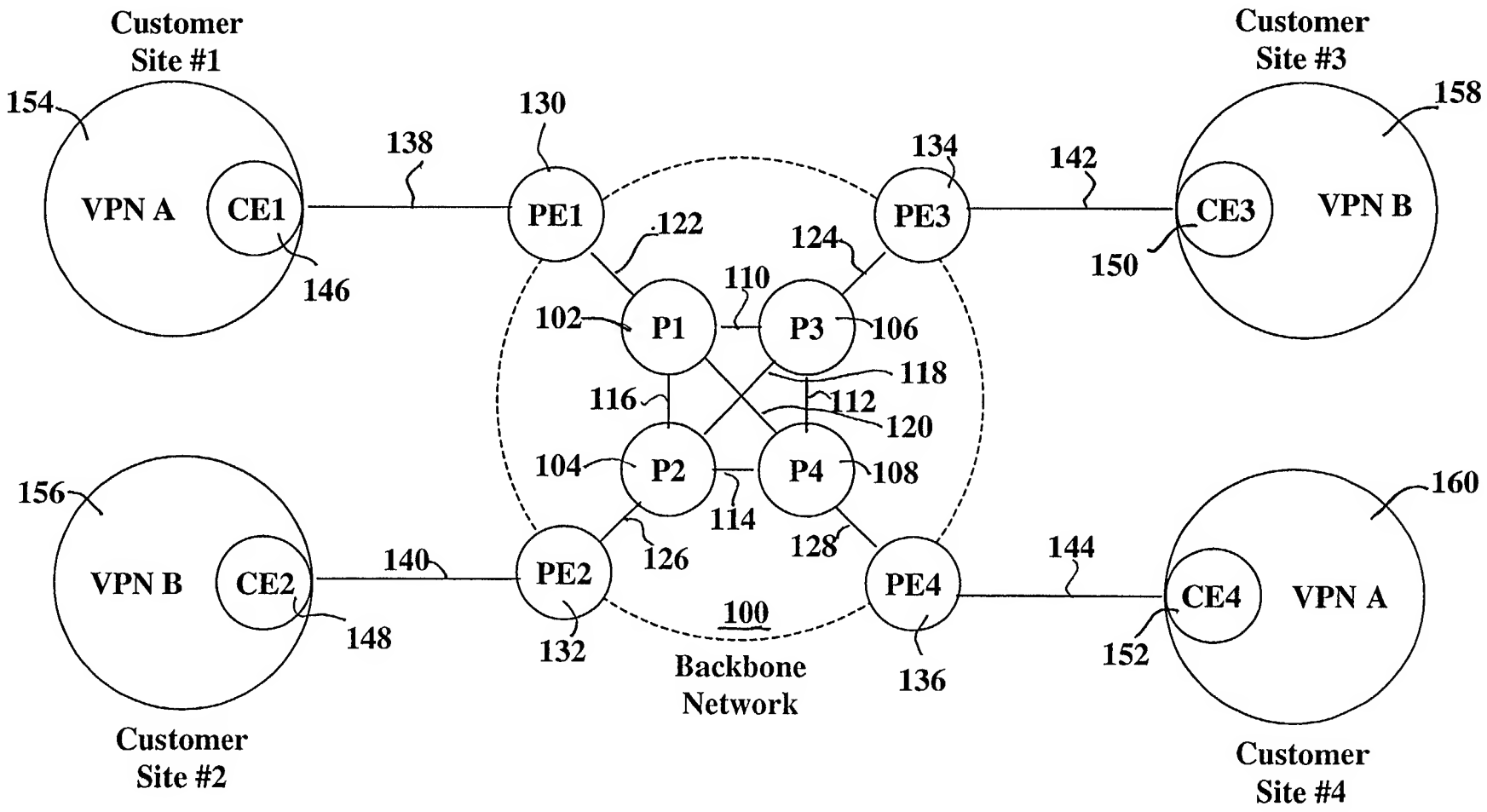
Fig. 1 **Prior Art**



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20ET20"h0292007

Fig. 2A

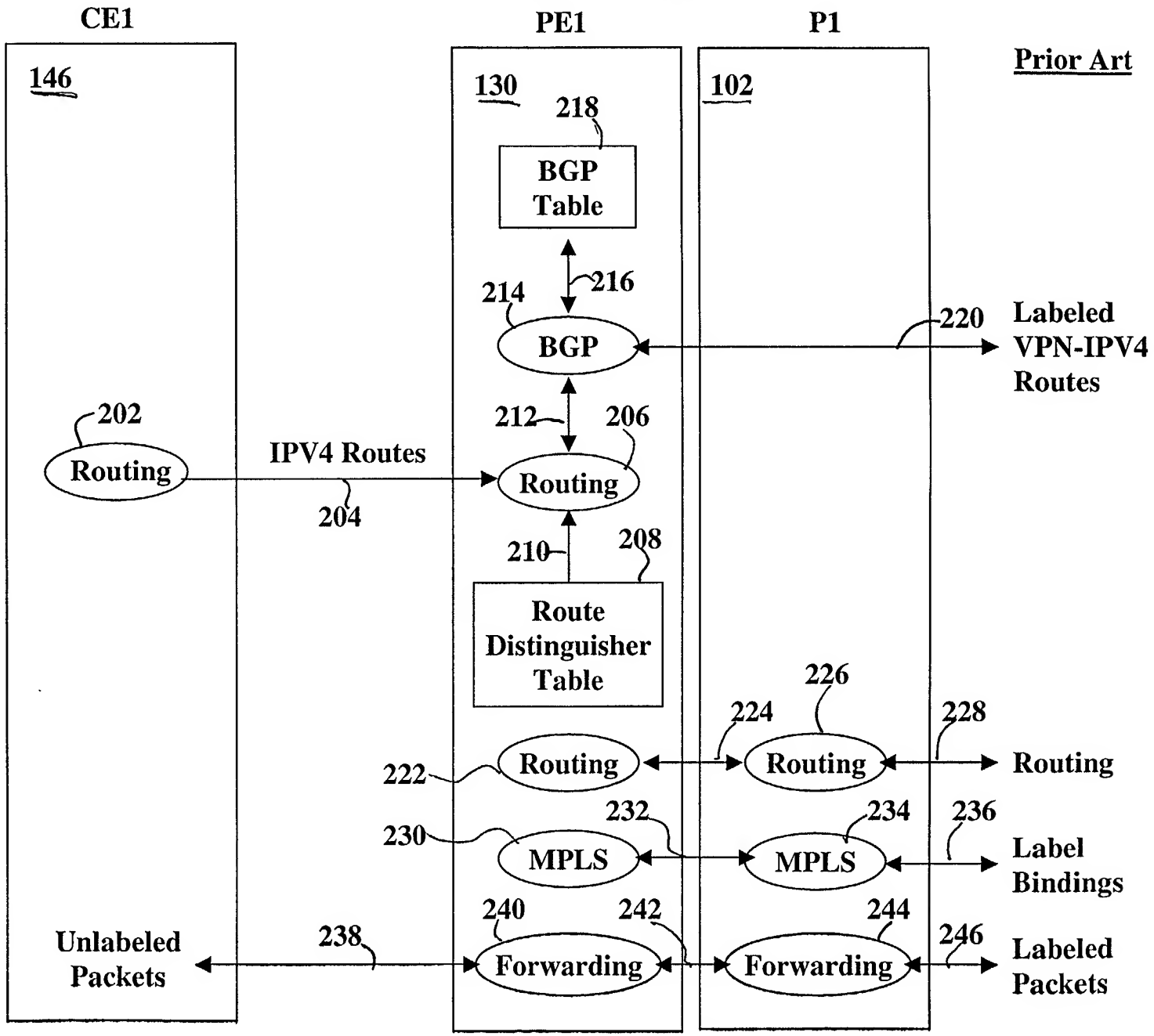

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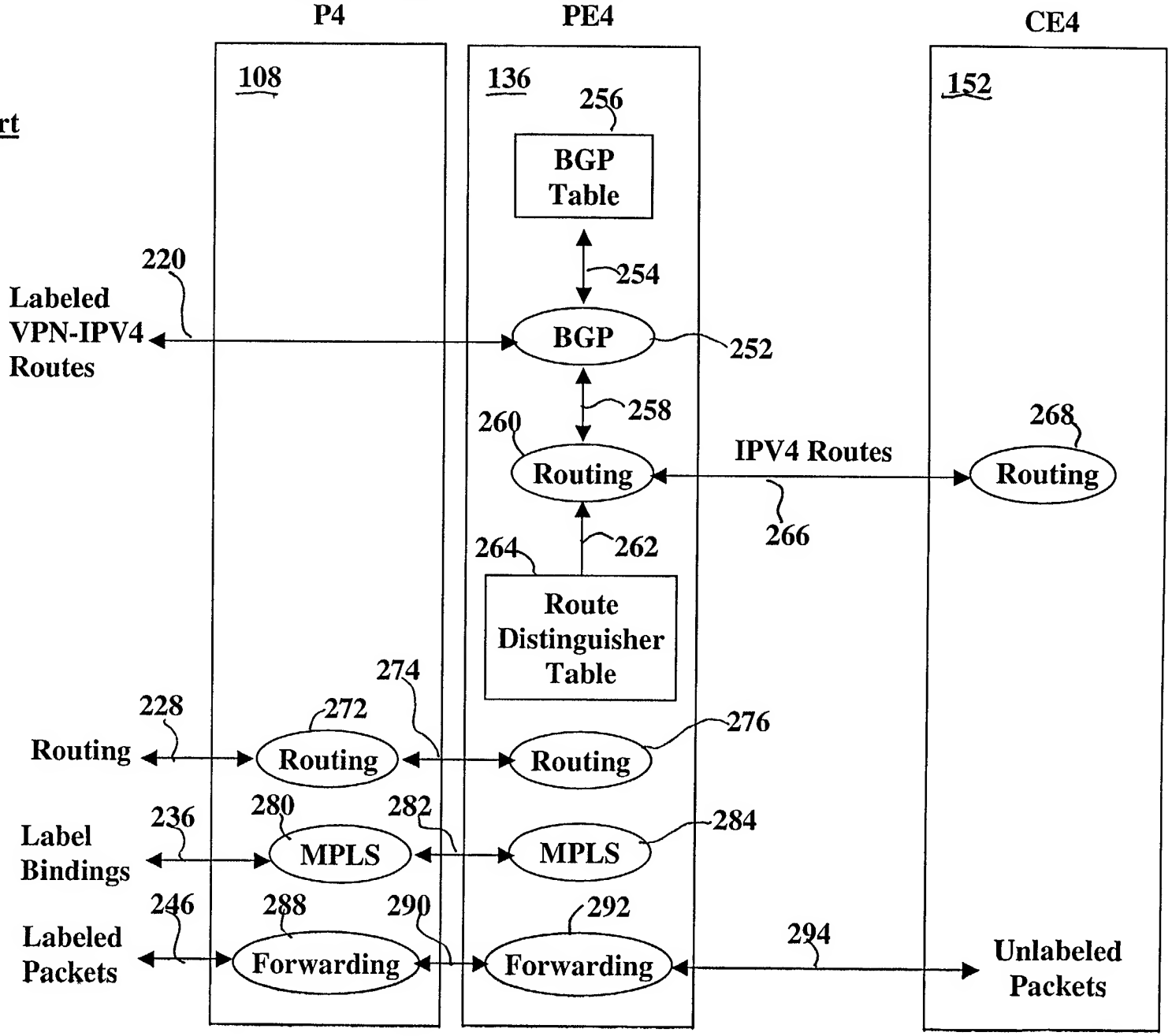
Fig. 2B

Prior Art



20ET20'h0292001

Fig. 3

208

302 Input Interface Address	304 Route Distinguisher
10.1.1.1	89:17

Prior Art

20ET20"h0292007

Fig.4

218

402 VPN-IPV4 Address	406 BGP Next Hop Address
89:17:10.5.5.5	20.0.0.1

Prior Art

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Fig. 5

Prior Art

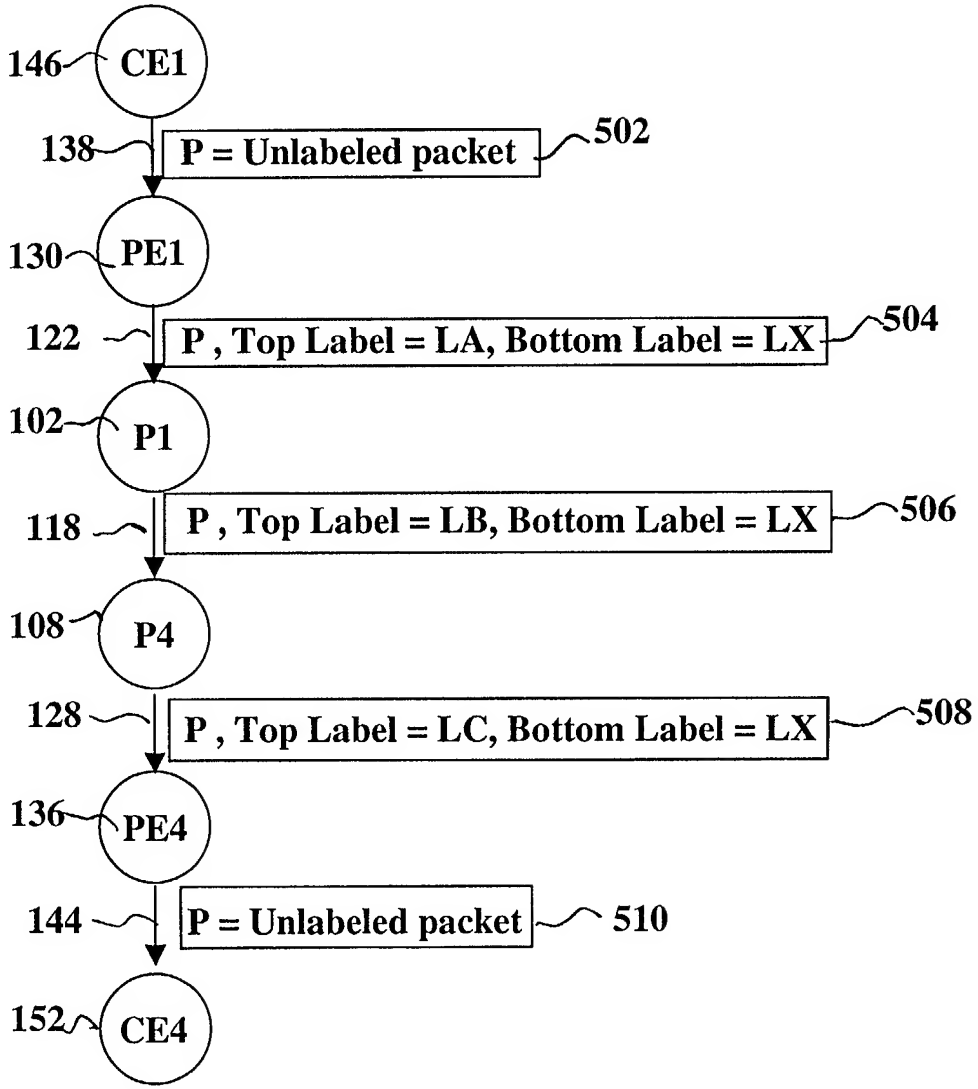

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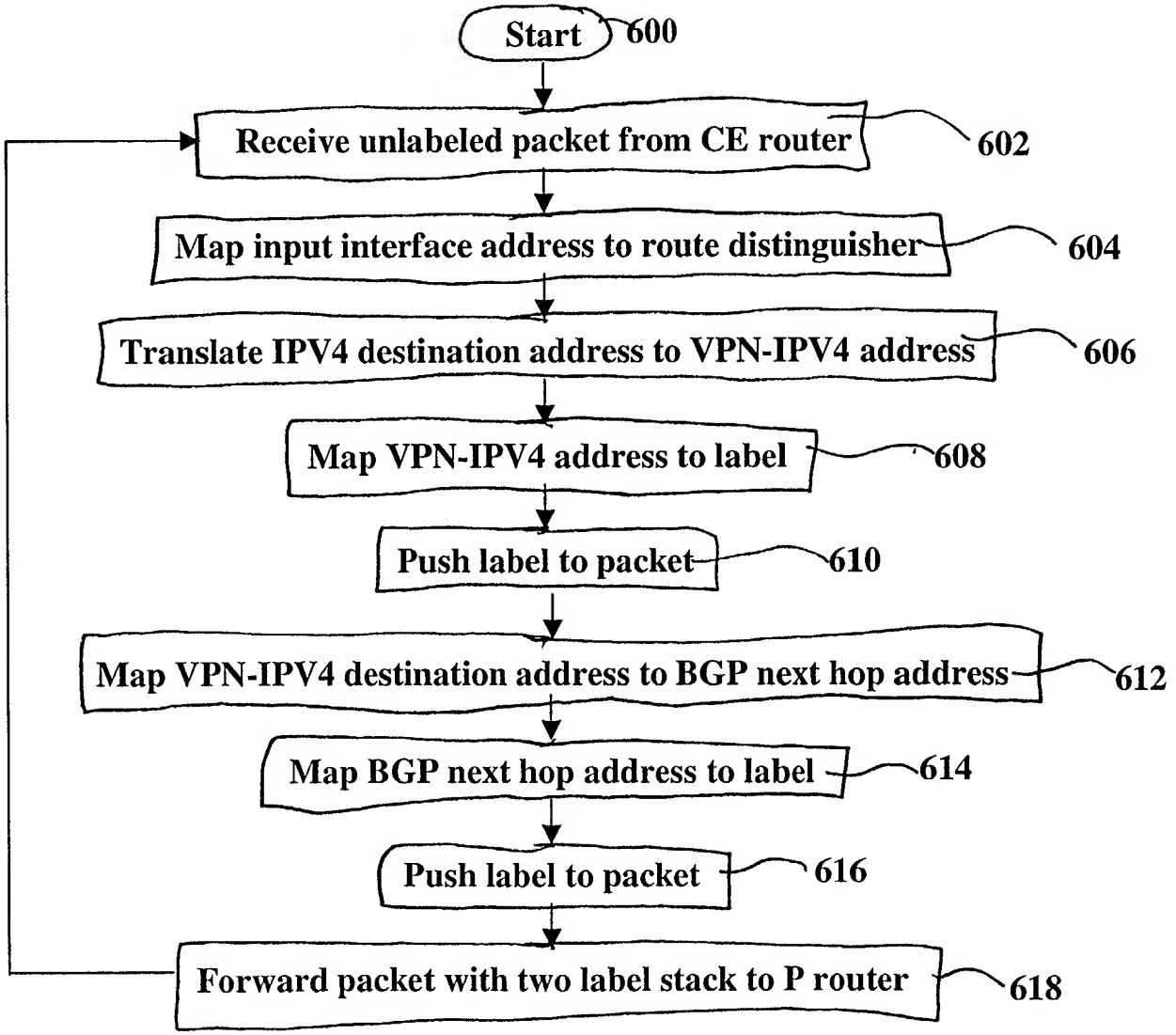
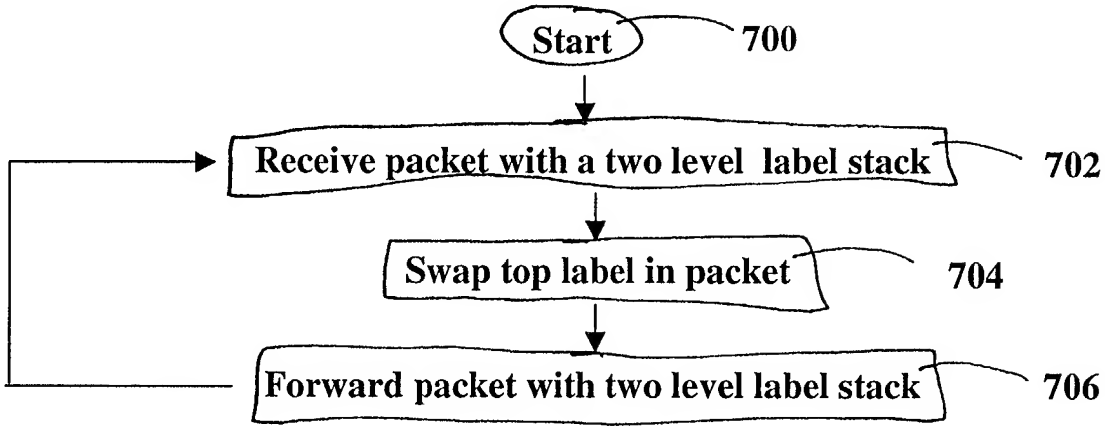
Fig. 6



Prior Art

20120"h0797007

Fig. 7



Prior Art

20EF20"h0Z92001

Fig.8

800

802 Input Interface Address	804 Input Label	806 Output Interface Address	808 Output Label
20.0.0.10	14	20.0.0.20	39

Prior Art

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Fig.9

Legend:
FRP = Flow Record Processor
MG = Matrix Generator

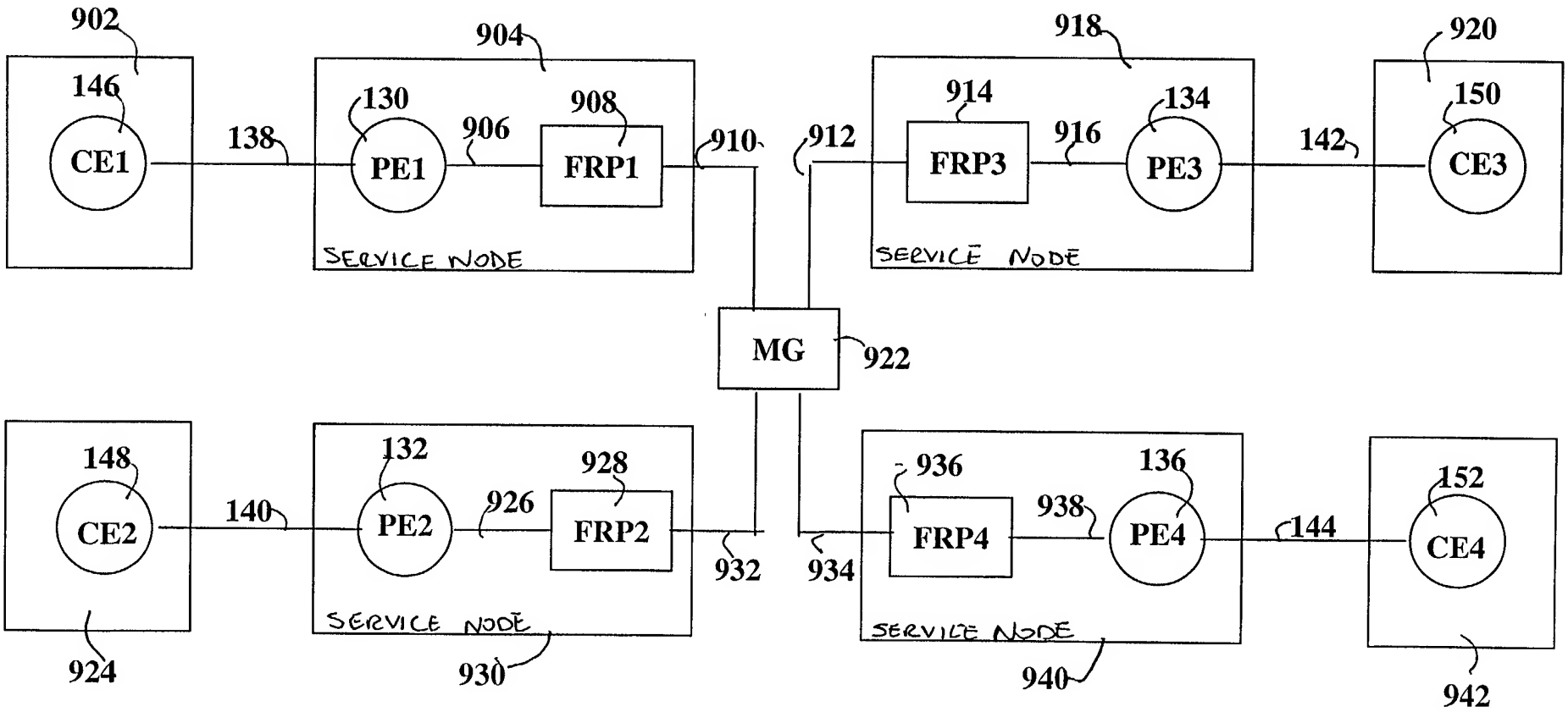

Fig. 10

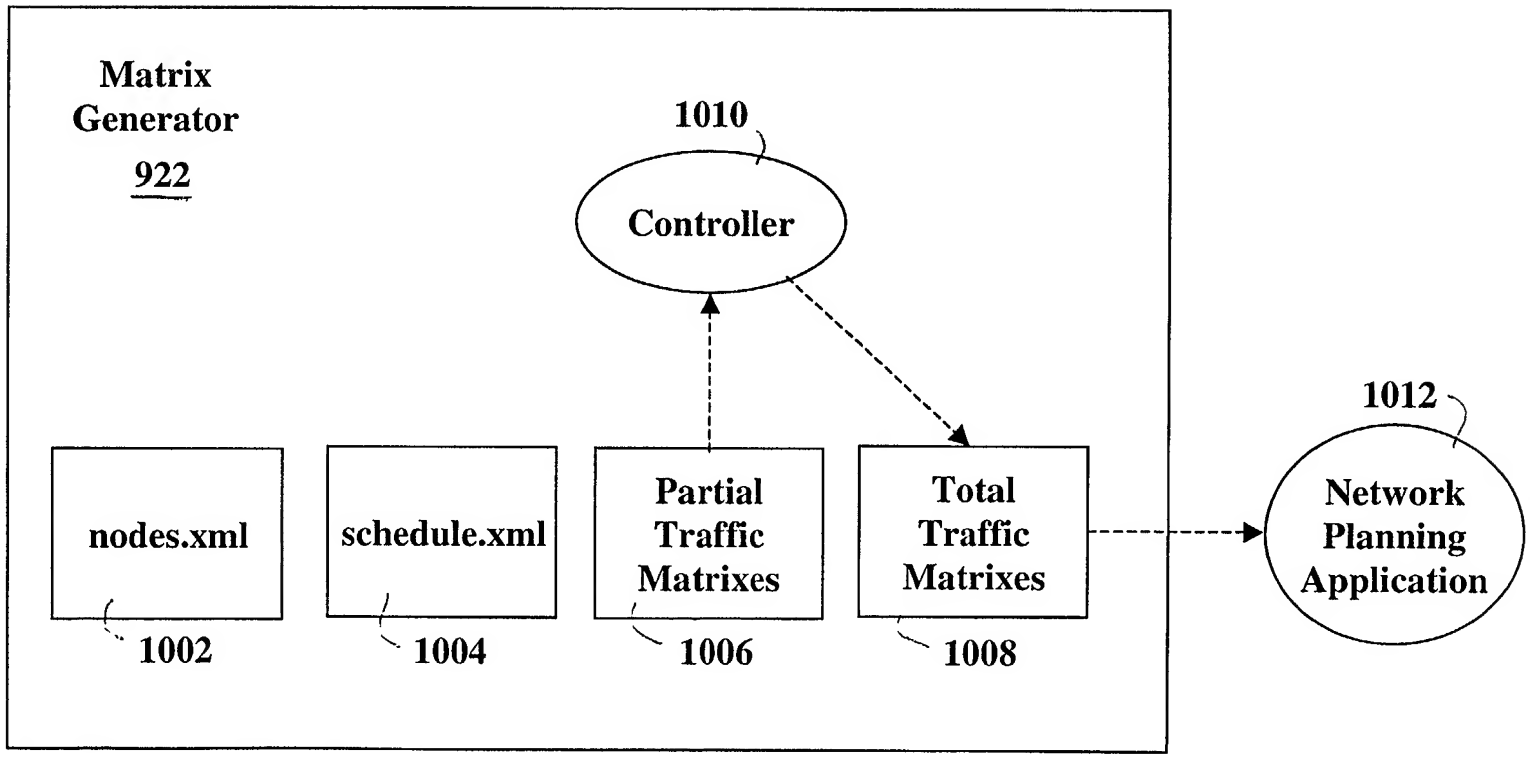
Matrix Generator
922
1010
Controller
1012
Network Planning Application
nodes.xml
schedule.xml
Partial Traffic Matrixes
Total Traffic Matrixes
1002
1004
1006
1008

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Fig. 11

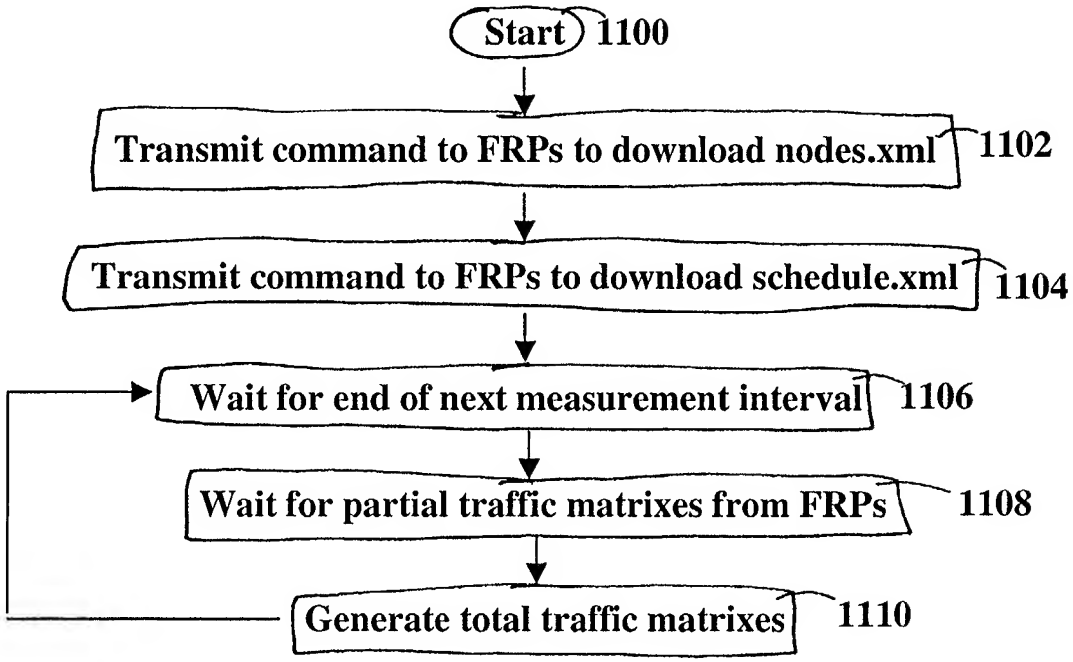
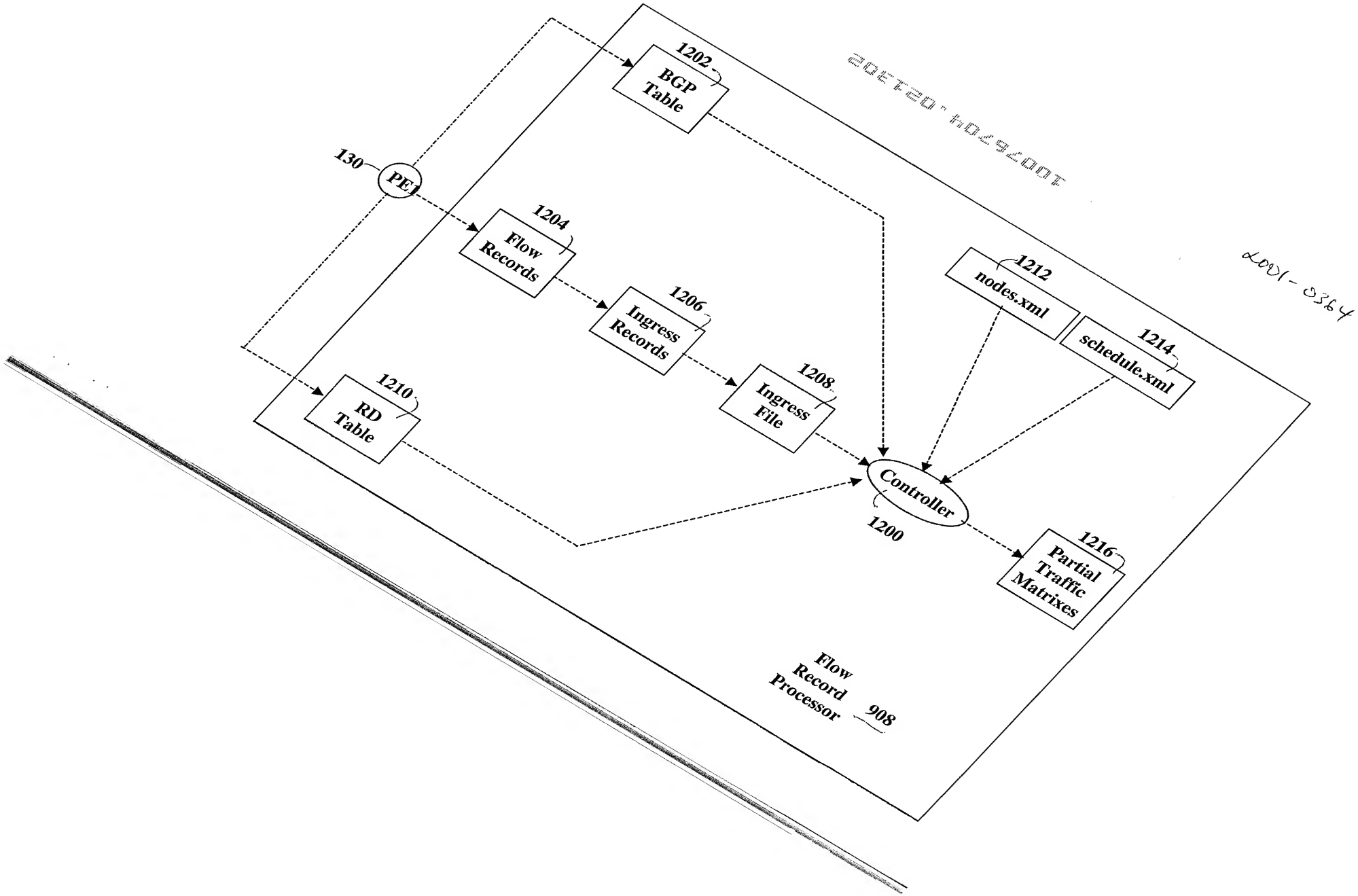
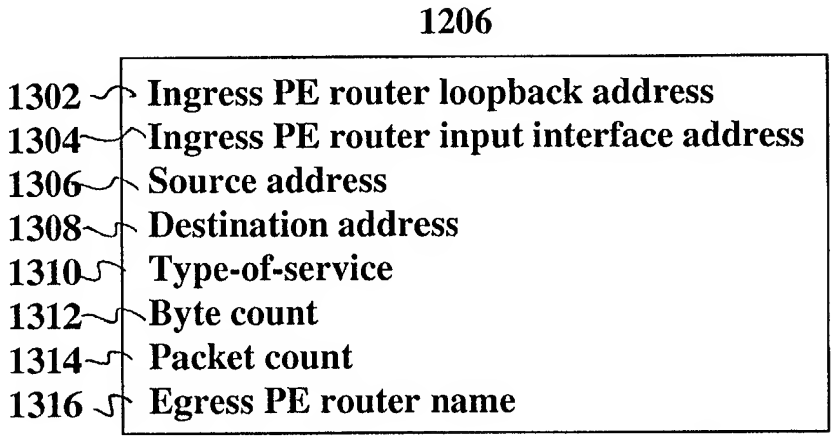


Fig. 12


1202
BGP Table
130
PE1
1204
Flow Records
1206
Ingress Records
1208
Ingress File
1210
RD Table
1212
nodes.xml
1214
schedule.xml
Controller
1200
1216
Partial Traffic Matrixes
Flow Record Processor
908

20020704.0292001

Fig. 13



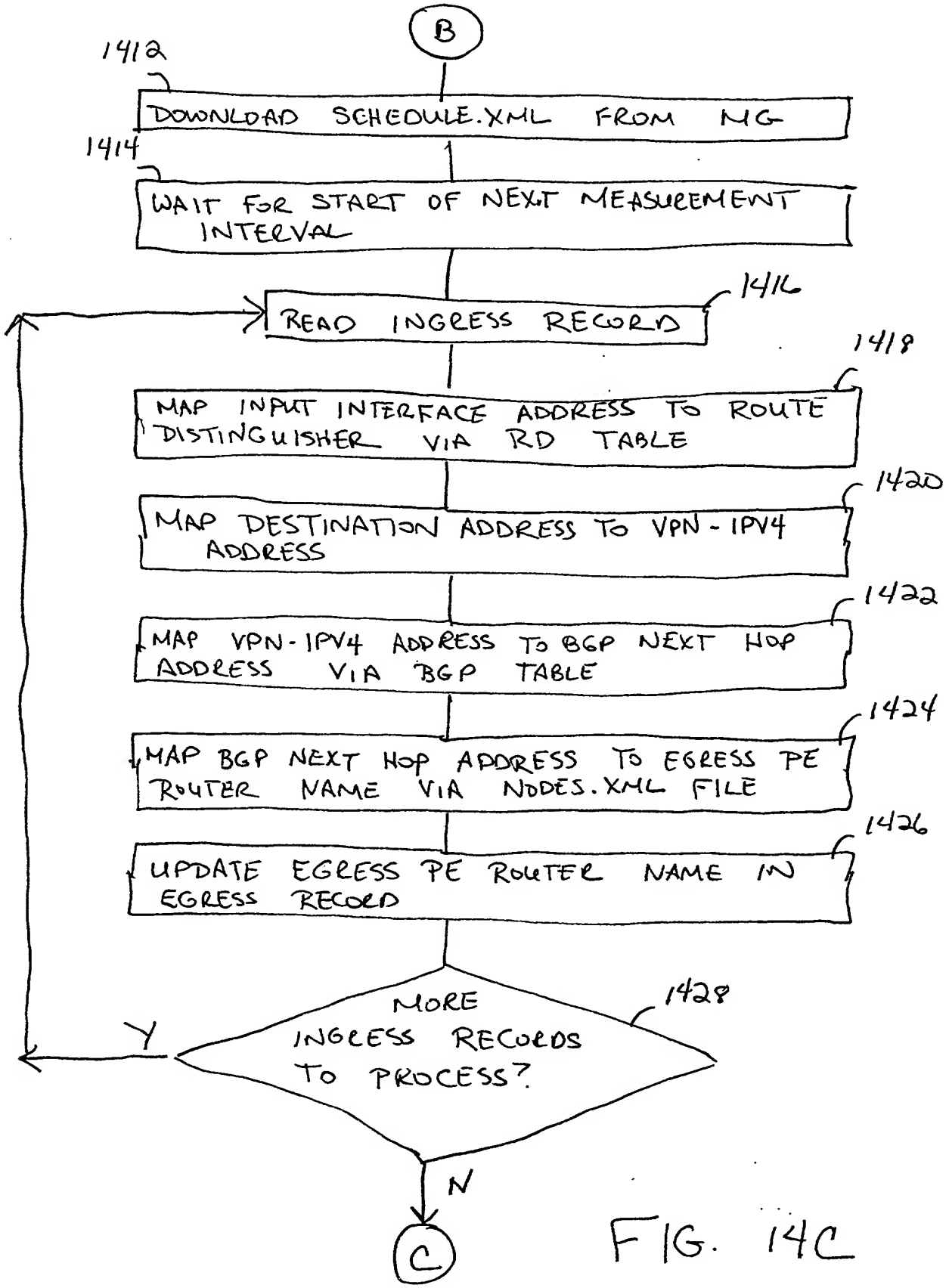


FIG. 14C

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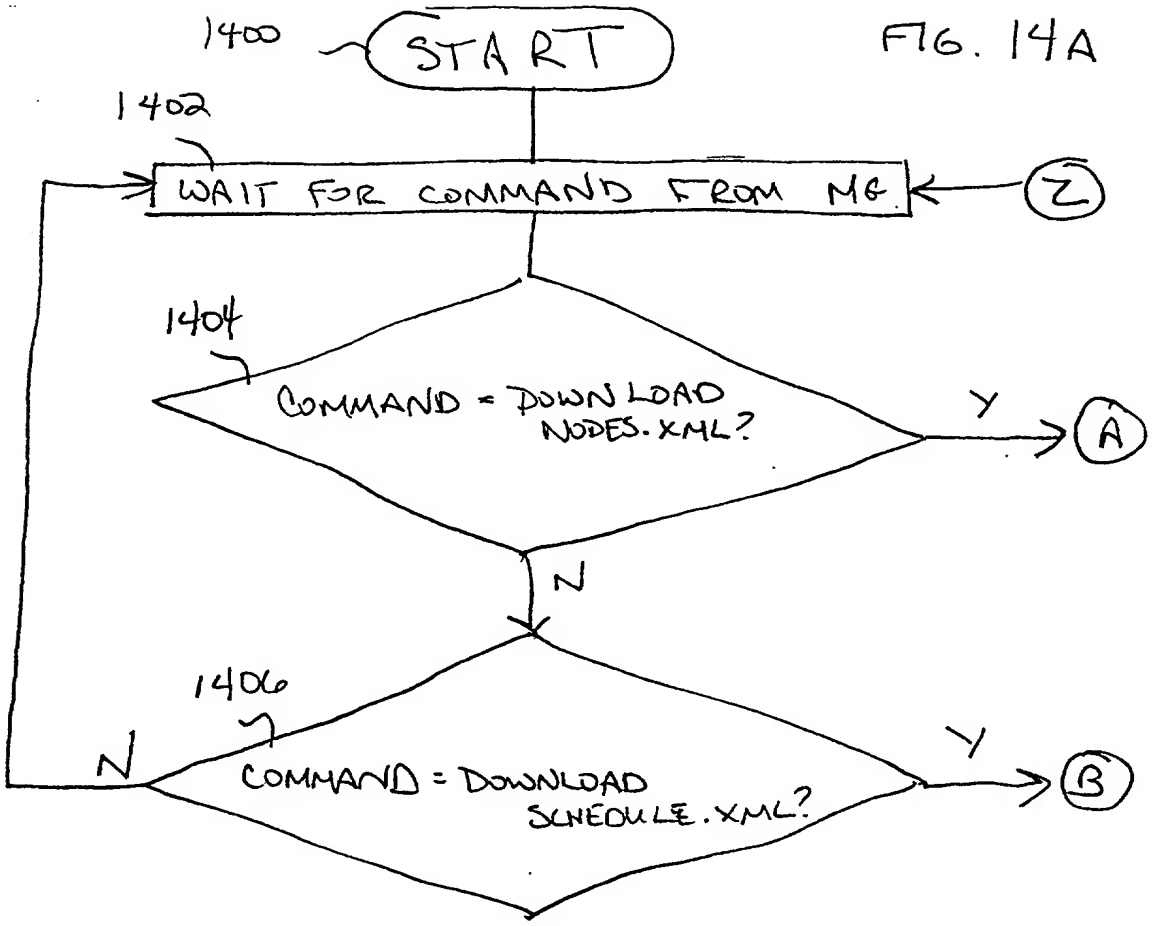

FIG. 14A

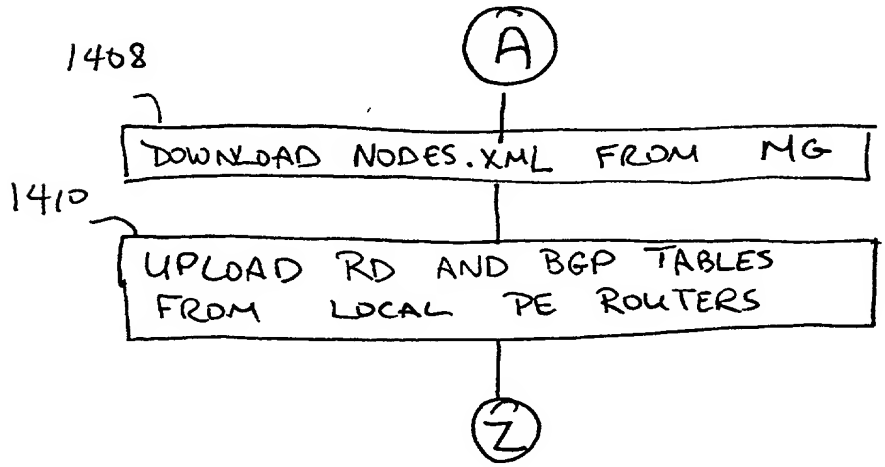

FIG 14.B

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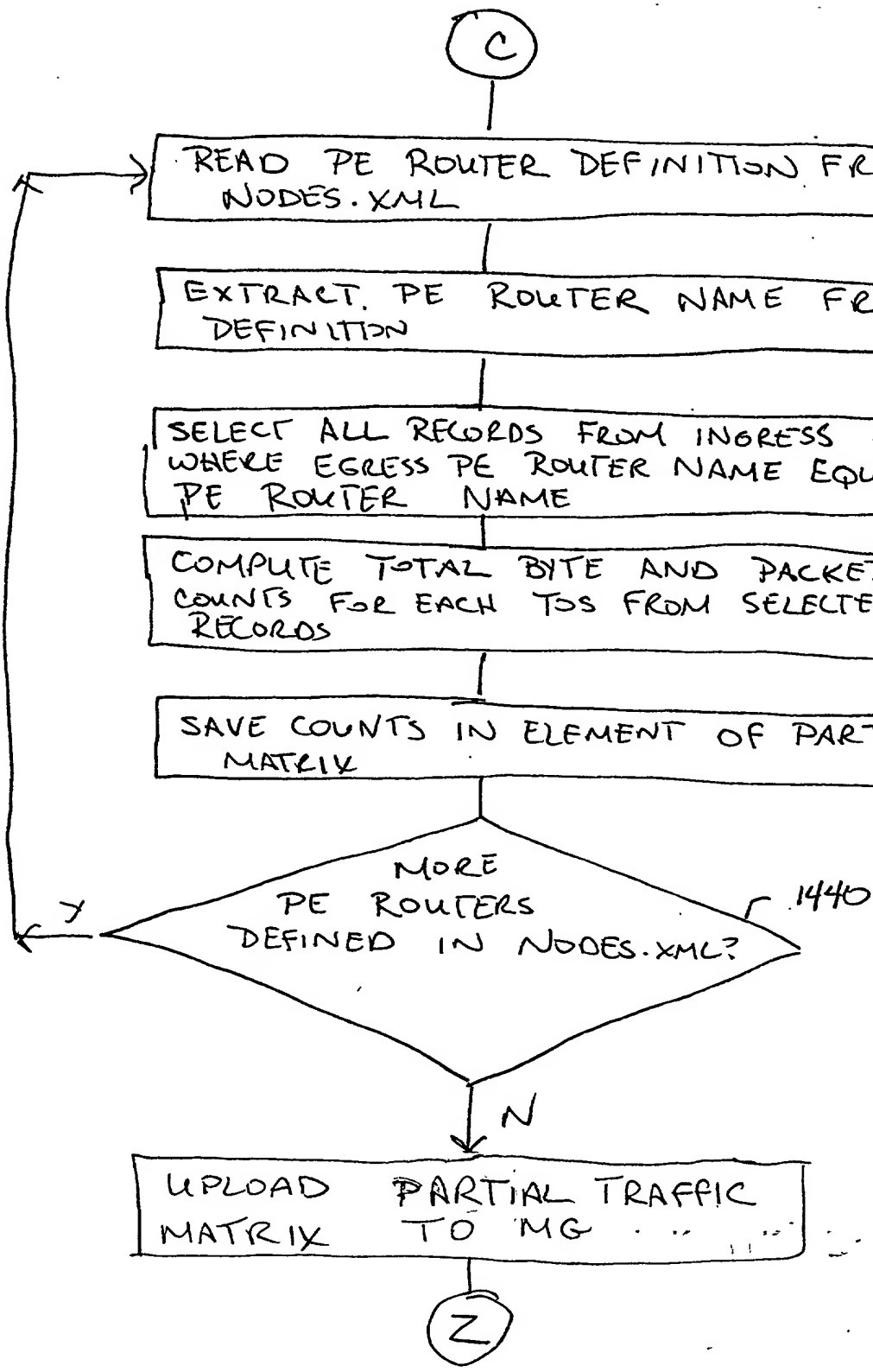

FIG. 14D

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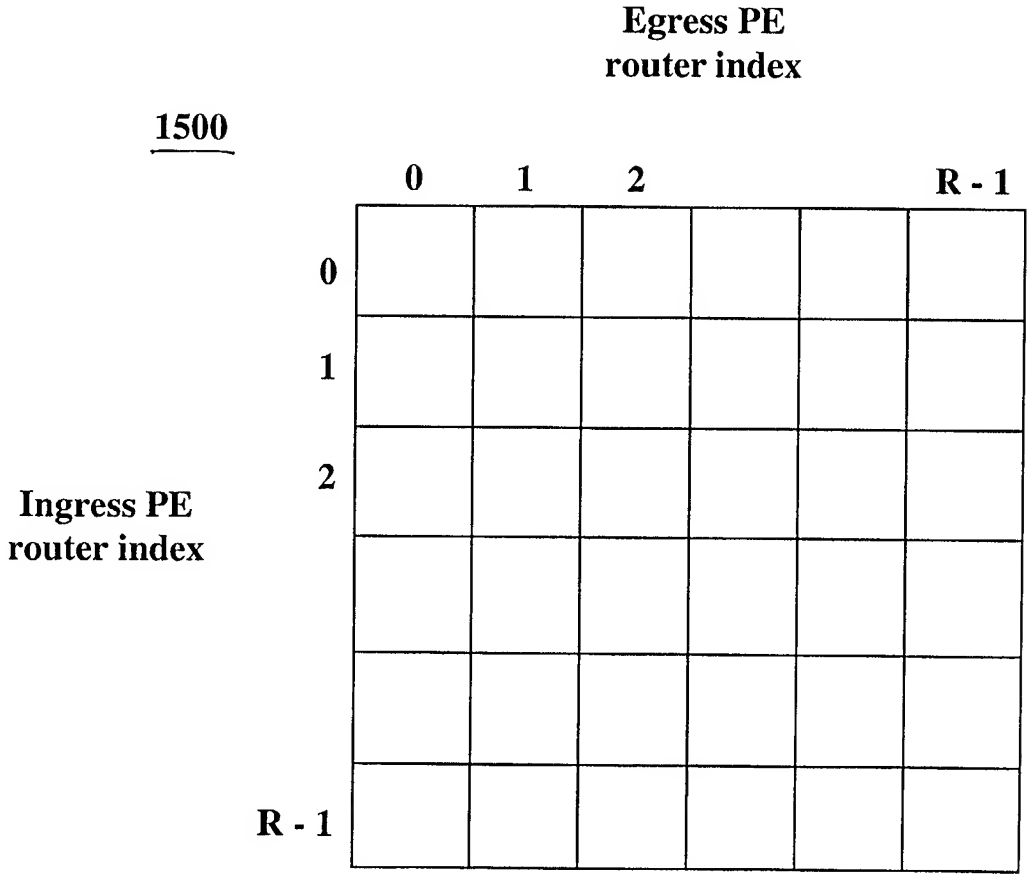
Fig. 15



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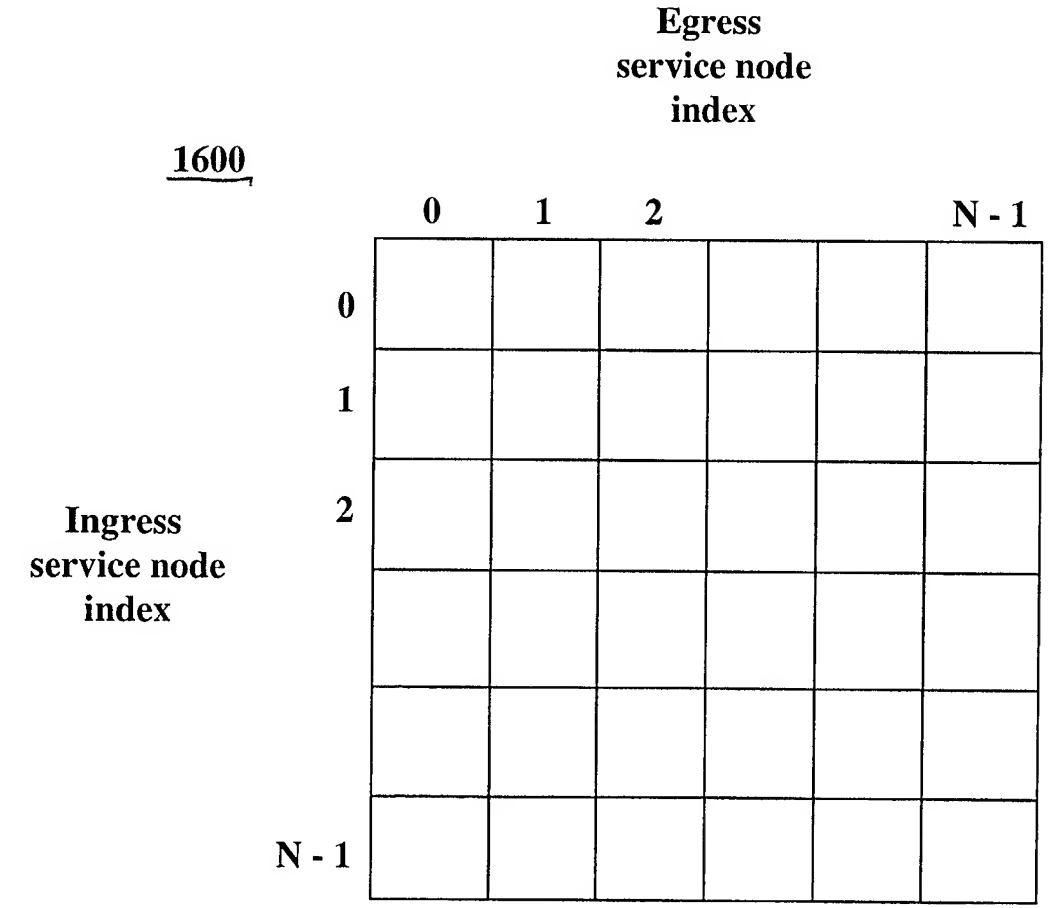
Fig. 16